

File No.: 9-14774-1US-1-1

November 20, 2003

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>In re Application of:</b>	<b>David F. SKOLL et al.</b>
<b>Filed:</b>	<b>Concurrently herewith.</b>
<b>Serial No.:</b>	<b>Not yet assigned. Division of Serial No. 09/927,551</b>
<b>Title:</b>	<b>DESIGN ANALYSIS WORKSTATION FOR ANALYZING INTEGRATED CIRCUITS</b>
<b>Examiner:</b>	<b>Not yet assigned.</b>
<b>Group Art Unit:</b>	<b>2825</b>

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**Mail Stop Non-Fee Amendment**

Commissioner for Patents  
United States Patent and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450  
U.S.A.

Sir:

**PRELIMINARY AMENDMENT**

As a preliminary amendment, please amend the above-identified patent application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.

Commissioner of Patents and Trademarks

November 20, 2003

**LISTING OF DISCLOSURE AMENDMENTS**

Please amend the first paragraph on page 1 of the specification as follows:

**CROSS-REFERENCE TO RELATED APPLICATIONS**

This application is a division of United States patent application Serial No. 09/927,551, which is a continuation of United States patent application Serial No. 09/690,813, filed October 18, 2000 and now abandoned.